NSF TUES DUE Type 2 Project:
Dissemination of Microprocessor Courses through Classroom and Interactive Cyber-Enabled Technologies
California State University-Fresno Distance Training Workshop Schedule

Day 1: Thursday: June 6, 2013

Date/Time (PST)	Item/Topic	Presenter	Location
6/6/13, 8:00AM-9:00AM	Introduction to Workshop/ Distance Training/Tools/ Project/Rules/Assessment/ Stipends, Pre-Test	**CSUF: Reza Raeisi** **ODU: Steve Hsiung** **ODU: John Ritz**	Any **Landline** **Internet**
6/6/13, 9:00AM-10:00AM	PIC MCU Architecture	**CSUF: Reza Raeisi** **ODU: Steve Hsiung**	**Any** **Landline** **Internet**
6/6/13, 10:00AM-12:00PM	Course & Lab Module #1: Header File, MPLAB Compilation, and PICkit2 Programming Exercises	**CSUF: Reza Raeisi** **ODU: Steve Hsiung** **Subgroups:** **ODU: Richard Jones** **WSU: Ece Yaprak** **TCC: Tom Stout**	Any **Landline** **Internet**
6/6/13, 12:00PM-1:00PM	Lunch Break	**CSUF: Reza Raeis**	
6/6/13, 1:00PM-1:30PM	Review of Learned Materials: Distance Tools, Course/Lab Module #1	**CSUF: Reza Raeisi** **ODU: Steve Hsiung**	Any **Landline** **Internet**
6/6/13, 1:30PM-2:30PM	Course Module #2: PIC Microcontroller Assembly Language Programming and MPLAB Simulation Exercises	**CSUF: Reza Raeisi** **ODU: Steve Hsiung**	Any Where **Landline** **Internet**
6/6/13, 2:30PM-3:30PM	Lab Module #2: PIC Microcontroller Assembly Language Programming and MPLAB Simulation Exercises	**CSUF: Reza Raeisi** **ODU: Steve Hsiung** **Subgroups:** **ODU: Richard Jones** **WSU: Ece Yaprak** **BRCC: Jim Eiland**	Any **Landline** **Internet**
6/6/13, 3:30PM-4:30PM	Course Module #3: PIC Microcontroller Index Assembly Language Programming & MPLAB Simulation Exercises	**CSUF: Reza Raeisi** **ODU: Steve Hsiung**	Any **Landline** **Internet**
6/6/13, 4:30PM-5:00PM	Course and Lab Modules #1-#3 Group Discussions/ Suggestions	**CSUF: Reza Raeisi** **ODU: Steve Hsiung, Richard Jones, John Ritz** **WSU: Ece Yaprak** **BRCC: Jim Eiland** **TCC: Tom Stout, Richard Seriani**	Any **Landline** **Internet**

These materials are based on work supported by the National Science Foundation under grant number: DUE 1120000.

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Day 2: Friday: June 7, 2013

Date/Time (PST)	Item/Topic	Presenter	Location
6/7/13, 8:00AM-9:00AM	Lab Module #3: PIC Microcontroller Index Assembly Language	**CSUF: Reza Raeisi ODU: Steve Hsiung Subgroups: ODU: Richard Jones, WSU: Ece Yaprak, TCC: Richard Seriani**	Any **Landline Internet**
6/7/13, 9:00AM-10:00AM	Course Module #4: PIC Microcontroller Index Assembly Language Programming & MPLAB Simulation Exercises	**CSUF: Reza Raeisi ODU: Steve Hsiung**	Any **Landline Internet**
6/7/13, 10:00AM-11:00AM	Lab Module #4: PIC Microcontroller Index Assembly Language Programming & MPLAB Simulation Exercises	**CSUF: Reza Raeisi ODU: Steve Hsiung Subgroups: ODU: Richard Jones, WSU: Ece Yaprak, TCC: Tom Stout**	Any **Landline Internet**
6/7/13, 11:00AM-12:00PM	Course Module #5: PIC Microcontroller I/O Controls Exercises	**CSUF: Reza Raeisi ODU: Steve Hsiung**	Any **Landline Internet**
6/7/13, 12:00PM-1:00PM	Lunch Break	**CSUF: Reza Raeisi**	
6/7/13, 1:00PM-2:00PM	Lab Module #5: PIC Microcontroller I/O Controls Exercises	**CSUF: Reza Raeisi ODU: Steve Hsiung Subgroups: ODU: Richard Jones, WSU: Ece Yaprak, BRCC: Jim Eiland**	Any **Landline Internet**
6/7/13, 2:00PM-2:30PM	Review of Learned Materials: Course/Lab Modules #3-#5	**CSUF: Reza Raeisi ODU: Steve Hsiung**	**Landline Internet**
6/7/13, 2:30PM-3:30PM	Course Module #6: I/O Controls with Interrupts Applications	**CSUF: Reza Raeisi ODU: Steve Hsiung,**	Any **Landline Internet**
6/7/13, 3:30PM-4:30PM	Lab Module #6: I/O Controls with Interrupts Applications	**CSUF: Reza Raeisi ODU: Steve Hsiung Subgroup: ODU: Richard Jones, WSU: Ece Yaprak, TCC: Richard Seriani**	Any **Landline Internet**
6/7/13, 4:30PM-5:00PM	Course and Lab Modules #3-#6 Group Discussions/ Suggestions	**CSUF: Reza Raeisi, ODU: Steve Hsiung, Richard Jones, John Ritz, WSU: Ece Yaprak, BRCC: Jim Eiland, TCC: Tom Stout, Richard Seriani**	Any **Landline Internet**

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NSF TUES DUE Type 2 Project:
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Day 3: Saturday: June 8, 2013

Date/Time (PST)	Item/Topic	Presenter	Location
6/8/13, 8:00AM-8:30AM	Review of Learned Materials: Course/Lab Modules #1-#6	**CSUF: Reza Raeisi ODU: Steve Hsiung**	**Any Landline Internet**
6/8/13, 8:30AM-10:00AM	Course Module #7: I/O Controls with LCD Module Parallel Communications and Table Lookup	**CSUF: Reza Raeisi ODU: Steve Hsiung**	Any **Landline Internet**
6/8/13, 10:00AM-11:30PM	Lab Module #7: I/O Controls with LCD Module Parallel Communications and Table Lookup/Video Demos	**CSUF: Reza Raeisi ODU: Steve Hsiung Subgroup: ODU: Richard Jones, WSU: Ece Yaprak, TCC: Tom Stout**	Any **Landline Internet**
6/8/13, 11:30AM-12:00PM	Course Module #8: Matrix Keypad Software Scanning, Debouncing & Decoding Design	**CSUF: Reza Raeisi ODU: Steve Hsiung**	Any **Landline Internet**
6/8/13, 12:00PM-1:00PM	Lunch Break	**CSUF: Reza Raeisi**	
6/8/13, 1:00AM-1:30PM	Review of Learned Materials: Course/Lab Modules #7	**CSUF: Reza Raeisi ODU: Steve Hsiung**	Any **Landline Internet**
6/8/13, 1:30PM-2:30PM	Course Module #8: Continue…….	**CSUF: Reza Raeisi ODU: Steve Hsiung**	**Landline Internet**
6/8/13, 2:30PM-4:00PM	Lab Module #8: Matrix Keypad Software Scanning, Debouncing & Decoding Design/Video Demos	**CSUF: Reza Raeisi ODU: Steve Hsiung Subgroup: ODU: Richard Jones, WSU: Ece Yaprak, BRCC: Jim Eiland**	Any **Landline Internet**
6/8/13, 4:00PM-4:30PM	Course/Lab Modules #9 & #10: Stepper & DC Motors Controls Introduction/Video Demos	**CSUF: Reza Raeisi ODU: Steve Hsiung**	Any **Landline Internet**
6/8/13, 4:30PM-5:00PM	Post-Test, Lab Project GroupDiscussions/Suggestions/Survey, Future on-line Meetings: Course Curriculum Review, Stepper Motor Module, DC Motor Module, Wireless RF Module via Moodle Server Forum and Listserve	**CSUF: Reza Raeisi, ODU: Steve Hsiung, Richard Jones, John Ritz, WSU: Ece Yaprak, BRCC: Jim Eiland, TCC: Tom Stout, Richard Seriani**	Any **Landline Internet**

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